

Ferroelectric Ba_{1-x}Sr_xTiO₃ Thin Films for DRAM's Applications

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Ferroelectric Ba_{1-x}Sr_xTiO₃ thin films were deposited by pulsed laser ablation on SiO₂/Si, RuO₂/Ta/SiO₂/Si and Pt/Ti/SiO₂/Si substrates. The films were weakly crystalline in the as-deposited condition and subsequent crystallization was induced by annealing the films in the range of 550-650°C. The BST films deposited on Pt/Ti/SiO₂/c-Si substrates presented wide cracks that were promoted during the annealing process due to the thermal expansion mismatch between the BST films ($\alpha_{\text{BST}} = 4 \times 10^{-6} \text{ }^\circ\text{C}^{-1}$) and the Pt ($\alpha_{\text{Pt}} = 9 \times 10^{-6} \text{ }^\circ\text{C}^{-1}$). Smooth films showing slightly cracked areas were obtained on SiO₂/c-Si and RuO₂/Ta/SiO₂/Si substrates. The ruthenium oxide thermal expansion coefficient is $\alpha_{\text{RuO}_2} = 5.2 \times 10^{-6} \text{ }^\circ\text{C}^{-1}$. A cross-sectional analysis at the ferroelectric/substrate interface showed that for the lower annealing temperature (550°C) a mixed amorphous/nanocrystalline microstructure is formed. For temperatures above 600°C a randomly oriented polycrystalline material is obtained. However, an amorphous layer of 4-6 nm still remains on the substrate even after heat-treatments up to 650°C. The dielectric constant of the BST films varied in the range of 30-325.

Keywords: pulsed laser ablation; ferroelectric films

1. Introduction

Ferroelectric oxide thin films with perovskite structure are currently of great technological interest due to their excellent properties for applications in dynamic random access memories (DRAM's)^[1]. Barium strontium titanate (BST) has a simple cubic perovskite structure with a lattice parameter of $a=0.39471 \text{ nm}$, space group Pm3m and possesses a high dielectric constant in the paraelectric state.

One of the main technological challenges is to find a suitable electrode material that could offer low electrical resistivity, good thermal stability, high resistance to oxidation and good adhesion both to the substrate and the ferroelectric film. Ruthenium oxide (RuO₂) is one of the most promising materials with only 35 $\Omega\text{-cm}$ resistivity, thermodynamic stability up to 800°C and chemically resistant to common acids and bases^[2].

BST thin films have been deposited by radio-frequency (RF) sputtering^[3,4], metallorganicchemical vapor deposition (MOCVD)^[5,6], ion beam sputtering^[7] and pulsed laser ablation^[1,8,9]. PLD is known to be an excellent technique to produce thin films on various substrates with preserving the stoichiometry of multicomponent targets^[10]. In the present study ferroelectric properties were measured on Ba_{0.5}Sr_{0.5}TiO₃ thin films grown by PLD and the crystallinity, morphology and microstructure were analyzed and compared with the target (bulk) properties

2. Experimental Procedure

Stoichiometric barium strontium titanate (Ba_{0.5}Sr_{0.5}TiO₃) targets were fabricated by combustion synthesized powders. The target for laser ablation experiments was made by isostatically pressing the ceramic powders and sintering in air at 1200°C for 2 hr. BST films

were grown by PLD on 400°C SiO₂/Si, RuO₂/Ta/SiO₂/Si and Pt/Ti/SiO₂/Si substrates with a KrF laser ($\lambda=248 \text{ nm}$) at a fluence energy of 2.0 J/cm² and 30 ns duration pulses. The separation between the rastered target and the rotated substrate was maintained fixed at 50 mm.

The BST/SiO₂/Si, BST/RuO₂/Ta/SiO₂/Si and BST/Pt/Ti/SiO₂/Si samples were cut into small pieces (2.0 x 2.0 cm²) and post-annealed in air for 2 hr. in the temperature range of 550-650°C.

The crystallinity of the films was determined by X-ray diffractometry (XRD) excited with Cu K α radiation ($\lambda=1.541 \text{ nm}$) and the surface morphology was examined by scanning electron microscopy (SEM).

High resolution transmission electron microscopy (HRTEM) was performed on cross-sectioned specimens in order to study the microstructure and thickness of the films. For capacitance measurements, platinum electrodes (0.1 mm in diameter) were deposited on top of the BST/substrate films by DC sputtering through a metallic mask. The dielectric constant was calculated from the capacitance measured at 10 kHz with a LCR meter by using the following equation:

$$C = \epsilon_0 \epsilon_r \frac{A}{d} \quad (1)$$

Where C is the capacitance (farads), ϵ_0 the free space dielectric constant value ($8.85 \times 10^{-12} \text{ F m}$), A the capacitor area (m²) and d. (m) the thickness of the ferroelectric film.

3. Results

Figures 1(a)-1(c) show the XRD patterns for the films grown on SiO₂/Si substrates and subsequently annealed at 550°C, 575°C and 600°C. The films were amorphous in the as-deposited condition and full re-crystallization was obtained at 550°C.

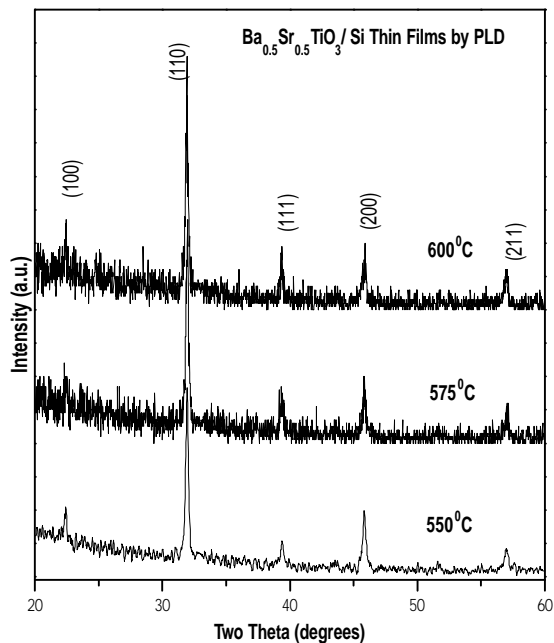


Figure 1. XRD patterns of BST/SiO₂/Si films post-annealed in the range 550-600°C for 2 hr

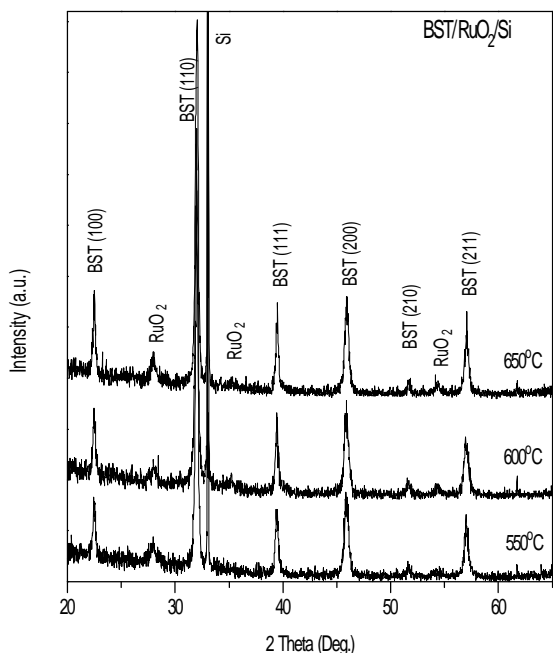


Fig. 2. XRD patterns of BST/ RuO₂/Ta/SiO₂/Si post-annealed at different temperatures in air for 2 hr.

Figures 2(a)-2(c) correspond to BST films grown on RuO₂/Ta/SiO₂/Si substrates and post-annealed in air for 2 hr in the range 550-650°C. The XRD patterns of the films match exactly with those obtained from the ablation target.

In this narrow temperature range (550-650°C) the films crystallized with a random orientation on both substrates. Similar results were obtained for BST films deposited on the Pt coated substrates.

Figures 3 and 4 show SEM micrographs for films grown on SiO₂/Si RuO₂/Ta/SiO₂/Si substrates, respectively. Smooth

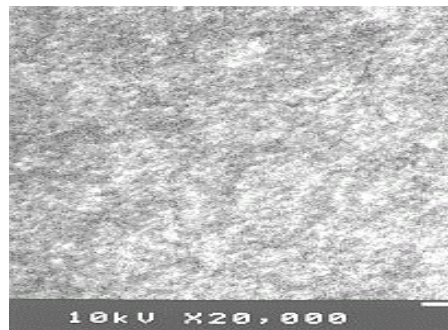


Fig. 3. SEM micrograph of BST/SiO₂/Si annealed at 600°C in air for 2 hr.

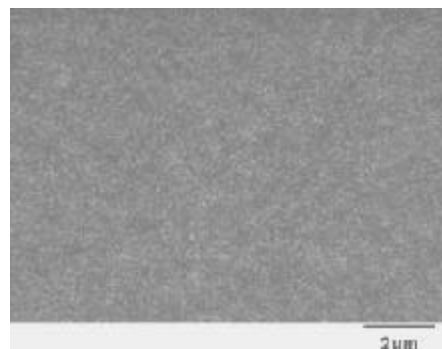


Figure 4. SEM micrograph of BST/RuO₂/Ta/SiO₂/Si annealed at 600°C in air for 2 hr.

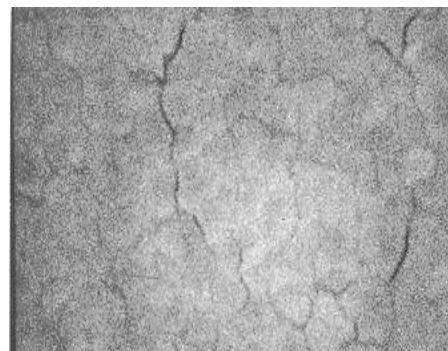


Fig. 5. SEM micrograph of BST/Pt/Ti/SiO₂/Si annealed at 600°C in air for 2 hr.

and uniform surfaces for both BST films that were post-annealed at 600°C in air for 2 hr can be observed. However for films grown on Pt/Ti/SiO₂/Si the surface area is cracked after the heat treatment, as can be seen in Figure 5.

The thermal expansion coefficients for SiO₂, RuO₂ and Pt are $\alpha_{\text{SiO}_2} = 3.5 \times 10^{-6} \text{ }^\circ\text{C}^{-1}$, $\alpha_{\text{RuO}_2} = 5.2 \times 10^{-6} \text{ }^\circ\text{C}^{-1}$, and $\alpha_{\text{Pt}} = 9 \times 10^{-6} \text{ }^\circ\text{C}^{-1}$, respectively. Assuming a thermal expansion coefficient value for BST to be similar to that reported for BaTiO₃ thin films^[11] ($\alpha_{\text{BaTiO}_3} = 4 \times 10^{-6} \text{ }^\circ\text{C}^{-1}$) it is reasonably to expect larger cracks on the Pt coated substrates due to a larger thermal expansion mismatch.

The as-deposited amorphous films crystallized with grain sizes between 50-200 nm after the post-annealing treatment. A cross-sectional HRTEM analysis at the

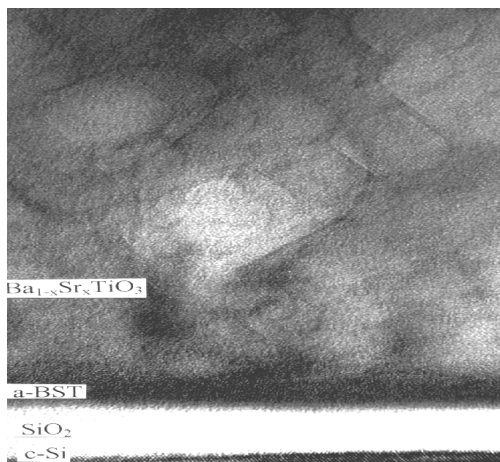


Fig. 6. Cross-sectional image taken from the BST/SiO₂/Si film post-annealed in air at 550°C for 2 hr.

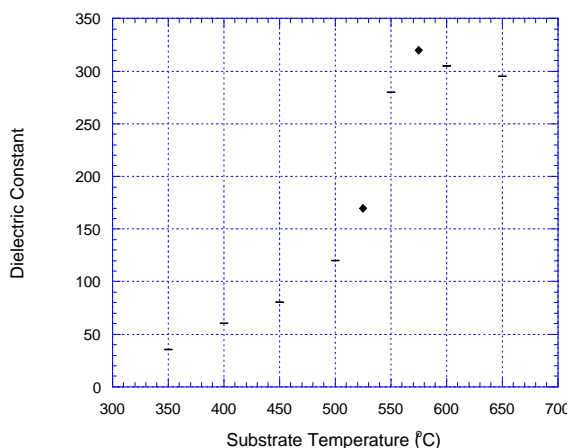


Fig. 7. Dielectric constant versus post-annealing temperature of BST/RuO₂/Ta/SiO₂/Si films.

film/substrate interface revealed that for samples annealed at 550°C a mixed amorphous/nanocrystalline phase is formed as can be seen in Figure 6. The film thickness is 200 nm. For temperatures above 600°C an amorphous layer of 4-6 nm is always observed at the BST/substrate interface.

The dependence of dielectric constant versus post-annealing temperature of BST films deposited on RuO₂/Ta/SiO₂/Si is shown in Figure 7. A maximum value of $\epsilon = 325$ can be obtained for films annealed at 575°C. Similar values were obtained for films deposited on Pt.

4. Conclusions

Ferroelectric Ba_{0.5}Sr_{0.5}TiO₃ (BST) thin films were obtained by pulsed laser deposition. The BST films were amorphous in the as-deposited condition and crystallized randomly under low temperature annealing (full crystallization from 550°C). The X-ray diffraction patterns of the films matched exactly with those obtained from the ablation target indicating the excellent stoichiometric

preservation is attained by the pulsed laser deposition. Smooth and uniform films with minimal cracking were obtained on SiO₂/Si, BST/RuO₂/Ta/SiO₂/Si substrates, while films deposited on Pt/Ti/SiO₂/Si substrates presented pronounced cracks that were promoted during the annealing process due to the thermal expansion mismatch between the film and the Pt substrates.

The as-deposited films re-crystallized on an amorphous BST layer (4-6nm) after post-annealing treatments in the temperature range of 550-650°C. A maximum dielectric constant $\epsilon = 325$ was obtained for BST samples post-annealed in air at 575°C for 2 hr.

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